





IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

09/905,218

Filing Date:

July 13, 2001

Confirmation No.:

4479

First Named Inventor:

Alexander J. Berger

Title:

ALIGNMENT OF SEMICONDUCTOR WAFERS AND OTHER

ARTICLES

Assignee:

Tru-Si Technologies, Inc.

Examiner:

Janice Lee Krizek

Art Unit:

3652

Attorney Docket No.:

M-11882 US

San Jose, California August 28, 2003

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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AMENDMENT

GROUP 3600

Dear Sir:

In reply to the Office Action dated July 9, 2003, please amend this application as

follows:

09/03/2003 LWBNDIM1 0000006 502257 09905218

01 FC:1202 02 FC:1201 234,00 DA 252,00 DA

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